CJD200 NPN CJD210 PNP

SURFACE MOUNT SILICON **COMPLEMENTARY POWER TRANSISTORS**



DPAK CASE

I_C=5.0A, I_B=1.0A

V_{CE}=1.0V, I_C=2.0A

 V_{CE} =1.0V, I_{C} =2.0A

 V_{CE} =2.0V, I_{C} =5.0A

 V_{CE} =1.0V, I_{C} =500mA

 V_{CE} =10V, I_{C} =100mA, f=10MHz

V_{CB}=10V, I_E=0, f=0.1MHz (CJD200)

V_{CB}=10V, I_E=0, f=0.1MHz (CJD210)

V_{BE}(SAT)

V_{BE}(ON)

hFE h_FE

hFE

 C_{ob}

 C_{ob}

 f_{T}



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UNITS

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CJD200 and CJD210 are complementary silicon power transistors manufactured in a surface mount package designed for high current amplifier applications.

MARKING: FULL PART NUMBER

2.5

1.6

180

80

120

MAXIMUM RATINGS:	(T _C =25°C unless	otherwise noted)
		SYMBOL

Collector-Base Voltage		V_{CBO}	40	V
Collector-Em	nitter Voltage	V_{CEO}	25	V
Emitter-Base	e Voltage	V_{EBO}	8.0	V
Continuous Collector Current		I _C	5.0	Α
Peak Collect	or Current	I _{CM}	10	Α
Continuous Base Current		Ι _Β	1.0	Α
Power Dissip	pation	P_{D}	12.5	W
Power Dissipation (T _A =25°C)		P_{D}	1.4	W
Operating and Storage Junction Temperature		T _J , T _{stg}	-65 to +150	°C
Thermal Resistance		ΘJC	10	°C/W
Thermal Resistance		$\Theta_{\sf JA}$	89.3	°C/W
ELECTRICA	L CHARACTERISTICS: (T _C =25°C	unless otherwise no	oted)	
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{CBO}	V _{CB} =40V		100	nA
I _{CBO}	V _{CB} =40V, T _C =125°C		100	μΑ
I _{EBO}	V _{EB} =8.0V		100	nA
BVCEO	I _C =10mA	25		V
VCE(SAT)	I _C =500mA, I _B =50mA		0.3	V
VCE(SAT)	I _C =2.0A, I _B =200mA		0.75	V
V _{CE} (SAT)	I _C =5.0A, I _B =1.0A		1.8	V

70

45

10

65

R3 (21-January 2013)

MHz рF

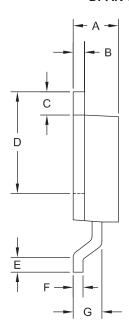
рF

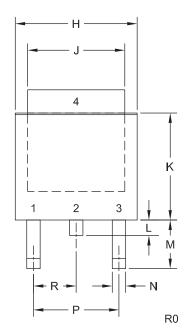
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SURFACE MOUNT SILICON COMPLEMENTARY POWER TRANSISTORS



DPAK CASE - MECHANICAL OUTLINE





LEAD CODE:

- 1) Base
- 2) Collector
- 3) Emitter
- 4) Collector

MARKING: FULL PART NUMBER

PHENGIONS								
DIMENSIONS								
	INCHES		MILLIMETERS					
SYMBOL	MIN	MAX	MIN	MAX				
Α	0.083	0.108	2.10	2.75				
В	0.016	0.032	0.40	0.81				
С	0.035	0.063	0.89	1.60				
D	0.203	0.228	5.15	5.79				
E	0.020	-	0.51	-				
F	0.018	0.024	0.45	0.60				
G	0.051	0.071	1.30	1.80				
Н	0.248	0.268	6.30	6.81				
J	0.197	0.217	5.00	5.50				
K	0.209	0.245	5.30	6.22				
L	0.025	0.040	0.64	1.02				
M	0.090	0.115	2.30	2.91				
N	0.012	0.045	0.30	1.14				
Р	0.180		4.60					
R	0.090		2.30					
DDAK (DEV DO)								

DPAK (REV: R0)

R3 (21-January 2013)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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