CJD122 NPN CJD127 PNP

SURFACE MOUNT SILICON COMPLEMENTARY POWER DARLINGTON TRANSISTORS



DPAK CASE



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CJD122 and CJD127 are complementary silicon power Darlington transistors manufactured in a surface mount package designed for low speed switching and amplifier applications.

MARKING: FULL PART NUMBER

MAXIMUM RATINGS:	(T _C =25°C unless otherwise noted))
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	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	100	V
Collector-Emitter Voltage	V_{CEO}	100	V
Emitter-Base Voltage	V_{EBO}	5.0	V
Continuous Collector Current	I_{C}	8.0	Α
Peak Collector Current	I _{CM}	16	Α
Continuous Base Current	ΙΒ	120	mA
Power Dissipation	P_{D}	20	W
Power Dissipation (T _A =25°C)	P_{D}	1.75	W
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	ΘJC	6.25	°C/W
Thermal Resistance	$\Theta_{\sf JA}$	71.4	°C/W

ELECTRICAL CHARACTERISTICS: $(T_C=25^{\circ}C \text{ unless otherwise noted})$

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
ICEO	V _{CE} =50V		10	μΑ
ICEV	V_{CE} =100V, $V_{BE(off)}$ =1.5V		10	μΑ
ICEV	V _{CE} =100V, V _{BE(off)} =1.5V, T _C =125	5°C	500	μΑ
I _{CBO}	V _{CB} =100V		10	μΑ
I _{EBO}	V _{EB} =5.0V		2.0	mA
BV _{CEO}	I _C =30mA	100		V
V _{CE} (SAT)	I _C =4.0A, I _B =16mA		2.0	V
VCE(SAT)	I _C =8.0A, I _B =80mA		4.0	V
V _{BE(SAT)}	I _C =8.0A, I _B =80mA		4.5	V
V _{BE(ON)}	V _{CE} =4.0V, I _C =4.0A		2.8	V
h _{FE}	V _{CE} =4.0V, I _C =4.0A	1000	12000	
h _{FE}	V_{CE} =4.0V, I_{C} =8.0A	100		
f_{T}	V_{CE} =4.0V, I_{C} =3.0A, f=1.0MHz	4.0		MHz
C _{ob}	V _{CB} =10V, I _E =0, f=1.0MHz (CJD122)		200	pF
C _{ob}	V _{CB} =10V, I _E =0, f=1.0MHz (CJD127)		300	pF
h _{fe}	V _{CE} =4.0V, I _C =3.0A, f=1.0kHz		300	

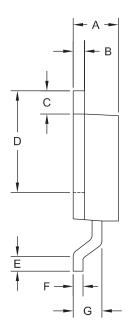
R3 (21-January 2013)

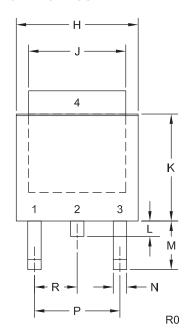
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DPAK CASE - MECHANICAL OUTLINE





LEAD CODE:

- 1) Base
- 2) Collector
- 3) Emitter
- 4) Collector

MARKING: FULL PART NUMBER

DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
Α	0.083	0.108	2.10	2.75		
В	0.016	0.032	0.40	0.81		
С	0.035	0.063	0.89	1.60		
D	0.203	0.228	5.15	5.79		
E	0.020	-	0.51	-		
F	0.018	0.024	0.45	0.60		
G	0.051	0.071	1.30	1.80		
Н	0.248	0.268	6.30	6.81		
J	0.197	0.217	5.00	5.50		
K	0.209	0.245	5.30	6.22		
L	0.025	0.040	0.64	1.02		
M	0.090	0.115	2.30	2.91		
N	0.012	0.045	0.30	1.14		
Р	0.180		4.60			
R	0.090		2.	30		
DPAK (REV: R0)						

DPAK (REV: R0)

R3 (21-January 2013)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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